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| EXPAND.USPT. | 128351 |
| EXPANDS.USPT. | 78435 |
| EXPANDING.USPT. | 91706 |
| EXPANDING.USPT. | 1 |
| EXTEND.USPT. | 808253 |
| EXTENDS.USPT. | 932907 |
| EXTENDING.USPT. | 1188878 |
| EXTENDINGS.USPT. | 78 |
| RANGE.USPT. | 1249062 |
| RANGES.USPT. | 270863 |
| AGP.USPT. | 998 |
| ((EXPAND OR EXPANDING OR EXTEND OR EXTENDING) ADJ4 RANGE) SAME (AGP OR GRAPHICS)).USPT. | 49 |

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| | | | |
|---|---|-----|------------|
| <u>L16</u> | ((expand or expanding or extend or extending) adj4 range) same (AGP or graphics) | 49 | <u>L16</u> |
| <u>L15</u> | l10 same (map or mapping or remap or remapping or translate or translating or convert or converting or conversion) same ((upper or high) adj2 (address)) | 8 | <u>L15</u> |
| <u>L14</u> | (translate or translating or convert or converting or expand or expanding or extend or extending) same address same l10 | 32 | <u>L14</u> |
| <u>L13</u> | l10 and l11 | 0 | <u>L13</u> |
| <u>L12</u> | L11 same l10 (translate or translating or convert or converting or expand or expanding or extend or extending) same ((large or larger) adj2 address) | 0 | <u>L12</u> |
| <u>L10</u> | GART or graphics address redirection table (translate or translating or convert or converting or conversion) same | 237 | <u>L10</u> |
| <u>L9</u> | (I/O or (I adj O) or peripheral or graphic or controller) same ((large or larger or extended or expanded) adj2 address) | 21 | <u>L9</u> |
| <u>L8</u> | L7 same (I/O or (I adj O) or peripheral or controller or graphic) (convert or translate or translating or translation or converting or conversion) same address same ((expand or expanding or expandable or extend or extendable or extension) adj3 address) | 16 | <u>L8</u> |
| <u>L6</u> | (convert or translate or translating or translation or converting or conversion) same address same ('4' adj GB) same ('64' adj GB) | 1 | <u>L6</u> |
| <u>L5</u> | ((expand or expanding or extend or extending) adj4 (address adj2 (range or space))) same (I/O or (I adj O) or peripheral or controller or graphics) same (larger or large) | 1 | <u>L5</u> |
| <u>L4</u> | ((expand or expanding or extend or extending or remap or remapping) adj4 (address adj2 (range or space))) same (I/O or (I adj O) or peripheral) | 20 | <u>L4</u> |
| <u>L3</u> | (expand or expanding or remap or remapping or extend or extending) same (I/O or (I adj O) or peripheral) same (address adj2 range) | 90 | <u>L3</u> |
| <u>L2</u> | L1 | 249 | <u>L2</u> |
| <i>DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i> | | | |
| <u>L1</u> | (expand or expanding or remap or remapping or map or mapping or extend or extending) same (I/O or (I adj O) or peripheral) same (address adj2 range) | 304 | <u>L1</u> |

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1 New techniques for efficient verification with implicitly conjoined 22%



BDDs

Alan J. Hu , Gary York , David L. Dill

Proceedings of the 31st annual conference on Design automation conference June 1994

2 Architectural implications of hardware-accelerated bucket 14%

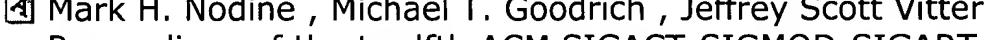


rendering on the PC

Michael Cox , Narendra Bhandari

Proceedings of the 1997 SIGGRAPH/Eurographics workshop on Graphics hardware August 1997

3 Blocking for external graph searching 8%



Mark H. Nodine , Michael T. Goodrich , Jeffrey Scott Vitter

Proceedings of the twelfth ACM SIGACT-SIGMOD-SIGART symposium on Principles of database systems August 1993

In this paper, we consider the problem of using disk blocks efficiently in searching graphs that are too large to fit in internal memory. Our model allows a vertex to be represented any number of times on the disk in order to take advantage of redundancy. We give matching upper and lower bounds for complete d-ary trees and d-dimensional grid graphs, as well as for classes of general graphs that intuitively speaking have a close to uniform number of neighbor

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1 Interfacing Ada to C — solutions to four problems 82%

Mitch Gart

Proceedings of the conference on TRI-Ada '95: Ada's role in global markets: solutions for a changing complex world November 1995

2 Comparison of access methods for time-evolving data 80%

Betty Salzberg , Vassilis J. Tsotras

ACM Computing Surveys (CSUR) June 1999

Volume 31 Issue 2

This paper compares different indexing techniques proposed for supporting efficient access to temporal data. The comparison is based on a collection of important performance criteria, including the space consumed, update processing, and query time for representative queries. The comparison is based on worst-case analysis, hence no assumptions on data distribution or query frequencies are made. When a number of methods have the same asymptotic worst-case behavior, features in the methods tha ...

3 Real world scene analysis in perspective 77%

Bruce L. Bullock

Proceedings of the 1975 annual conference January 1975

This paper examines the applicability of current scene analysis techniques to real world problems. The majority of the current techniques have been developed for simple scenes with straight lines, simple shapes, good contrast, and little texture. This paper shows several examples illustrating that many of these techniques

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| | | | result set |
| <u>L22</u> | L21 and 14 | 0 | <u>L22</u> |
| <u>L21</u> | bus controller same (translation lookaside buffer or tlb) | 53 | <u>L21</u> |
| <u>L20</u> | l12 adj5 14 | 2 | <u>L20</u> |
| <u>L19</u> | ((remap or remapping) adj4 (l12 adj2 address)) | 3 | <u>L19</u> |
| <u>L18</u> | (remap or remapping) same (l12 adj2 address) same (outside or above or higher) same (l12 adj2 (space or range)) | 3 | <u>L18</u> |
| <u>L17</u> | L16 same (expand or expanding or increase or increasing or enlarge or extend or extending or remap or remapping or map or mapping) same ('4') same ('64') | 14 | <u>L17</u> |
| <u>L16</u> | l12 adj4 (l11 or address) | 6657 | <u>L16</u> |
| <u>L15</u> | L13 same (expand or expanding or increase or increasing or enlarge or extend or extending or remap or remapping) | 65 | <u>L15</u> |
| <u>L14</u> | L13 same (expand or expanding or increase or increasing or enlarge or extend or extending) | 33 | <u>L14</u> |
| <u>L13</u> | L12 adj4 l11 | 1182 | <u>L13</u> |
| <u>L12</u> | I/O or (I adj O) or peripheral | 434457 | <u>L12</u> |
| <u>L11</u> | address adj2 (space or range) | 15452 | <u>L11</u> |
| <u>L10</u> | l9 same (TLB or GART) | 1 | <u>L10</u> |
| <u>L9</u> | (table or buffer) same ((convert or translate or translating or converting or remap or remapping) adj4 (l1 adj2 (address or request))) | 42 | <u>L9</u> |
| <u>L8</u> | L7 | 1954 | <u>L8</u> |
| <i>DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i> | | | |
| <u>L7</u> | (table or buffer) same (convert or translate or translating or converting or remap or remapping) same l1 | 2502 | <u>L7</u> |
| <u>L6</u> | l1 same l4 | 16 | <u>L6</u> |
| <u>L5</u> | l3 and l2 | 301 | <u>L5</u> |
| <u>L4</u> | L3 or l2 | 307 | <u>L4</u> |
| <u>L3</u> | graphic address redirection table or GART | 301 | <u>L3</u> |
| <u>L2</u> | graphic address remapping table or GART | 307 | <u>L2</u> |
| <u>L1</u> | I/O or I adj O or peripheral | 972614 | <u>L1</u> |

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| <u>L22</u> | L21 and l4 | 0 | <u>L22</u> |
| <u>L21</u> | bus controller same (translation lookaside buffer or tlb) | 53 | <u>L21</u> |
| <u>L20</u> | l12 adj5 l4 | 2 | <u>L20</u> |
| <u>L19</u> | ((remap or remapping) adj4 (l12 adj2 address)) | 3 | <u>L19</u> |
| <u>L18</u> | (remap or remapping) same (l12 adj2 address) same (outside or above or higher) same (l12 adj2 (space or range)) | 3 | <u>L18</u> |
| <u>L17</u> | L16 same (expand or expanding or increase or increasing or enlarge or extend or extending or remap or remapping or map or mapping) same ('4') same ('64') | 14 | <u>L17</u> |
| <u>L16</u> | l12 adj4 (l11 or address) | 6657 | <u>L16</u> |
| <u>L15</u> | L13 same (expand or expanding or increase or increasing or enlarge or extend or extending or remap or remapping) | 65 | <u>L15</u> |
| <u>L14</u> | L13 same (expand or expanding or increase or increasing or enlarge or extend or extending) | 33 | <u>L14</u> |
| <u>L13</u> | L12 adj4 l11 | 1182 | <u>L13</u> |
| <u>L12</u> | I/O or (I adj O) or peripheral | 434457 | <u>L12</u> |
| <u>L11</u> | address adj2 (space or range) | 15452 | <u>L11</u> |
| <u>L10</u> | l9 same (TLB or GART) | 1 | <u>L10</u> |
| <u>L9</u> | (table or buffer) same ((convert or translate or translating or converting or remap or remapping) adj4 (l1 adj2 (address or request))) | 42 | <u>L9</u> |
| <u>L8</u> | L7 | 1954 | <u>L8</u> |

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| | | | |
|-----------|--|--------|-----------|
| <u>L7</u> | (table or buffer) same (convert or translate or translating or converting or remap or remapping) same l1 | 2502 | <u>L7</u> |
| <u>L6</u> | l1 same l4 | 16 | <u>L6</u> |
| <u>L5</u> | l3 and l2 | 301 | <u>L5</u> |
| <u>L4</u> | L3 or l2 | 307 | <u>L4</u> |
| <u>L3</u> | graphic address redirection table or GART | 301 | <u>L3</u> |
| <u>L2</u> | graphic address remapping table or GART | 307 | <u>L2</u> |
| <u>L1</u> | I/O or I adj O or peripheral | 972614 | <u>L1</u> |

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